EXAMINER'S AMENDMENT

1.   An examiner's amendment to the record appears below. Should the changes

and/or additions be unacceptable to applicant, an amendment may be filed as provided by

37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no

later than the payment of the issue fee.

2.   Authorization for this examiner's amendment was given in a telephone interview

with Mr. Maurice j. Pirio, Reg. No. 33273, on 05/19/2008.

3.   In the claims:

      a. Replace claim 23 with the following:

Claim 23. A method in a computer system for a task to exit a known state, the

computer system having an operating system and supporting multiple streams,

the task having multiple threads executing on the streams that are in the

known state, the known state being an active state in which the thread is not

making productive use of the processor, the method comprising:

    under control of the operating system,

        notifying a designated thread of the task to exit the known state;

    under control of the designated thread of the task,

        notifying each of the other threads of the task executing on a

parallel processor architecture having multiple simultaneously executing

protection domains to exit the know state; and

after notifying each of the other threads, executing instructions that were to be executed prior to entering the know state to effect the exiting of the known state; and

for each of the threads,

in response to receiving the notification, executing instructions that were to be executed prior to entering the known state to effect the exiting of the known state.

b. Replace claim 34 with the following:

Claim 34. A system for placing a task in a know state, the task having multiple threads executing on streams of the system, the system comprising:

a central processing unit;

an operating system;

a component under control of the operating system for notifying each of the threads of the task executing on a parallel processor architecture having multiple simultaneous executing streams to enter a known state, the known state being an active state in which the thread is not making productive use of the processor;

a component for each of the threads that,

in response to receiving the notification, causes the thread to enter the known state so that an action can be performed with the task being in the known state; and

notifies a designated thread of the task that the thread has entered a

known state; and

a component for the designated thread that,

when a notification has been received from each of the others

threads, notifies the operating system that the task is now in the known state.

c. Replace claim 56 with the following:

Claim 56. A system for use by a task to exit a known state, the system

supporting multiple streams and including an operating system and a central

processing unit, the task having multiple threads executing on the streams that

are in the known state, the known state being an active state in which the

thread is not making productive use of the processor, the system comprising:

a component under control of the operating system for notifying a

designated thread of the task to exit the known state, wherein the task is

executing on a parallel processor architecture having multiple simultaneous

executing protection domain;

a component of the designated thread for notifying each of the other

threads of the task to exit the known state and that after notifying each of the

other threads, causes the designated thread to execute instructions that were to

be executed prior to entering the known state to effect the exiting of the

known state; and

a component for each thread that,

in response to receiving the notification, executes instructions that were to be executed prior to entering the known state to effect the exiting of the known state.

4.   Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jennifer N. To whose telephone number is (571) 272-7212. The examiner can normally be reached on M-T 7AM- 4:30 PM, F 7AM- 3:30 PM.

5.   If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached on (571) 272-3756. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

6.   Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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